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Electronic Patent Application Submission
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EFS ID: 54006

Application ID: 10686243



Title of Invention:

METHOD AND APPARATUS OF A
SMART DECODING SCHEME FOR
FAST SYNCHRONOUS READ IN A
MEMORY SYSTEM

First Named Inventor: Vikram Kowshik

Domestic/Foreign Application: Domestic Application

Filing Date: 2003-10-14

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Submission Type: Information Disclosure
Statement

Filing Type:

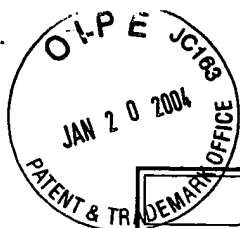
Confirmation number: 7611

Attorney Docket Number: ATM-279

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Digital Certificate Holder: cn=Thomas Schneck,ou=Registered Attorneys,ou=Patent and Trademark
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Electronic Version v1.1

Stylesheet Version v1.1.0


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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Thomas Schneck Registered Number: 24518</td><td>/thomas schneck/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Thomas Schneck Registered Number: 24518	/thomas schneck/	Attorney		
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<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>ATM279ids-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	ATM279ids-usidst.xml		us-ids.dtd		us-ids.xsl
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us-ids	ATM279ids-usidst.xml									
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Comments										



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

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